

## AVR JTAG Debugger and ISP Programmer with USB Connection

AVR USB  
JTAG ISP



The AVR JTAG-ISP is an all-in-one developer solution for programming and debugging Atmel AVR series microcontrollers. With this single device most member of the 8-bit AVR microcontroller series can be programmed and some of them can be debugged. The AVR JTAG-ISP has one USB interface for communication with the PC. The installation is very straightforward after a few seconds of installing necessary drivers the device is ready for programming or debugging. No switching is necessary because the device automatically recognises ISP or JTAG ICE commands and switches to the appropriate mode. The USB interface emulates a serial port so this device is fully compatible with many programmer software which uses AVR JTAG ICE or AVRISP protocol. It's also fully compatible with many development environment like AVR Studio. The AVR JTAG-ISP has the following features:

- AVR JTAG ICE emulation
- AVRISP (STK500) v2 emulation
- Automatic switching between ISP and JTAG ICE mode
- 10 pin header for JTAG and ISP connection with standard pinout
- USB interface to the PC
- Powered from USB connection
- Compatible only with 5V systems

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